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PATENT NUMBER and
ISSUE DATE

U.S. ~~UTILITY~~ Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10053031	01/17/2002	365 711	51	2894 2185	

****APPLICANTS:** Anderson David;

****CONTINUING DATA VERIFIED:**
THIS APPLN CLAIMS BENEFIT OF 60/262,472 01/17/2001

**** FOREIGN APPLICATIONS VERIFIED:**

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		H0002113	
Verified and Acknowledged Examiners's initials			
TITLE : Enhanced memory module architecture			

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Applicati n Examiner	
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